

**IN THE SPECIFICATION**

Please amend the paragraph beginning on page 14, line 20 as follows:

Figure 5 is a flowchart showing how to integrate dynamic function loading in spreadsheets on small devices driven from a desktop computer according to another embodiment of the present invention. First, at step 500, the user selects a spreadsheet file from a desktop computer to download to a PDA. Next, at step 510, the selected spreadsheet is examined for the types of functions and formulas it contains. Next, at step 520, it is determined whether the spreadsheet program on the PDA has the user's desired functions. If the spreadsheet program has the user's desired functions, at step ~~570~~, 520, the user operates the spreadsheet application on the PDA with the available functions after downloading the spreadsheet file from the desktop to the PDA at step 560. If the spreadsheet program does not have the user's desired functions, at step 530, the user's input is analyzed to determine whether the user wants the entire function package. If the user wants the entire function package, at step 550, the entire function package is downloaded into the user's PDA. If the user does not want the entire function package, at step ~~540~~, 530, only the user's desired functions are downloaded into the user's PDA. After either step 540 or 550 the spreadsheet file is downloaded from the desktop to the PDA at step 560. Finally, at step 570, the user operates the spreadsheet application on the PDA with the downloaded functions.

Please amend the paragraph beginning on page 21, line 15 as follows:

In one embodiment of the invention, the processor 913 is a microprocessor manufactured by ~~Moterola~~, MOTOROLA, such as the 680X0 processor or a microprocessor manufactured by ~~Intel~~, INTEL, such as the 80X86, or ~~Pentium~~ PENTIUM processor, or a SPARC microprocessor

from ~~Sun Microsystems, Inc.~~ SUN MICROSYSTEMS, Inc. However, any other suitable microprocessor or microcomputer may be utilized. Main memory 915 is comprised of dynamic random access memory (DRAM). Video memory 914 is a dual-ported video random access memory. One port of the video memory 914 is coupled to video amplifier 916. The video amplifier 916 is used to drive the cathode ray tube (CRT) raster monitor 917. Video amplifier 916 is well known in the art and may be implemented by any suitable apparatus. This circuitry converts pixel data stored in video memory 914 to a raster signal suitable for use by monitor 917. Monitor 917 is a type of monitor suitable for displaying graphic images.

Please amend the paragraph beginning on page 19, line 21 as follows:

Network link 921 typically provides data communication through one or more networks to other data devices. For example, network link 921 may provide a connection through local network 922 to local server computer 923 ("HOST") or to data equipment operated by ISP 924. ISP 924 in turn provides data communication services through the world wide packet data communication network now commonly referred to as the "Internet" 925. Local network 922 and Internet 925 both use electrical, electromagnetic or optical signals, which carry digital data streams. The signals through the various networks and the signals on network link 921 and through communication interface 920, which carry the digital data to and from computer 900, are exemplary forms of carrier waves transporting the information.